



Shri Mata Vaishno Devi University
Department of Electronics & Communication Engineering
Sub Post Office, Katra 182 320, Jammu and Kashmir, India
(Recognized under section 12(B) & 2(f) of UGC Act, 1956)

SMVDU/DoECE/17/969

Date: 11 Dec. 2017

Notification

List of provisionally shortlisted candidates (GATE and Non GATE) for selection process for Admission to Ph.D. Electronics and Communication Engineering(ECE) Program, Dec 2017-May 2018 in SMVDU

1. The list of GATE and Non GATE candidates who have been provisionally shortlisted for the selection process for admission in Dec. 2017-May 2018 Session of 2017-18 to Ph.D. Programs in ECE on **19th December 2017** in SMVDU are given below.
2. All the shortlisted candidates are required to report admission cell at 09.30 AM on **19th December 2017** in C. Block (Department of Electronics and Communication Engineering), SMVDU and bring the following documents in original with self attested copies for verification and record :
 - a. Mark sheet of all the Semesters of the qualifying examinations (as per eligibility criterion)
 - b. Date of Birth Certificate (Matriculation)
 - c. Provisional Passing certificate of Bachelor's Degree
 - d. Provisional passing certificate of Masters' Degree (if feasible)
 - e. GATE Scorecard in original (For GATE qualified candidates)
 - f. Identity Proof
3. All the GATE shortlisted students are hereby informed that they are required to be present for the Interview /presentation & Viva on **19th Dec. 2017** in the Conference Hall, Department of ECE, SMVDU. All the Non - GATE shortlisted students are to be present for the written test on **19th Dec. 2017** followed by the personal interview/presentation & Viva.
4. The list of provisionally selected candidates will be displayed on university website on or after 21st Dec. 2017 for submission of fees as per schedule given in admission brochure.

S. No	Name of the Candidate	Father's Name	GATE /Non- GATE
1	SHWETA CHAUHAN	JAI PARKASH CHAUHAN	NON- GATE
2	PHOGAT RITU RANBIRSINGH	RANBIRSINGH PHOGAT	NON- GATE
3	MAHENDRA KUMAR SRIVASTAV	RAJESH SRIVASTAV	GATE

Sd/-
HoD SECE