Shri Mata Vaishno Devi University
Result of B. Tech (ECE)
Batch 2015 (Summer , 2019)

Entry no	Student Name	Sem	Cgpa	Sgpa	Credits Earned	Backlog List
15BEC006	AKHIL SHARMA	8.5	6.22	5	161	* ECL 2030 Linear Integrated Circuits & Applications(4) * ECL 2040 Electromagnetic Field Theory(4) * ECL 2071 Digital System Design using VHDL(4) * ECP 2070 Digital Electronics Lab(1) * ECP 2201 Electronics Workshop Lab (1) * PCN 1010 NSS(0)
15BEC014	NAVITA	8.5	6.24	4	131	* CSL 1021 Introdcution to C Programming(3) * ECL 1022 Network Analysis & Synthesis(4) * ECL 2030 Linear Integrated Circuits & Applications(4) * ECL 2040 Electromagnetic Field Theory(4) * ECL 2060 Microprocessor Systems(4) * ECL 2070 Digital Electronics(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 2151 Analog Communication Engineering(4) * ECL 2152 Digital Communication Engineering(4) * ECL 3080 Embedded Electronics & Microcontrollers(4) * ECL 3180 Signal Processing & Linear Systems(4) * ECP 2060 Microprocessor Lab(1) * ECP 2071 VHDL Lab(1
15BEC019	SAKET PURI	8.5	5.61	4.5	138	* ECL 2040 Electromagnetic Field Theory(4) * ECL 2070 Digital Electronics(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 3080 Embedded Electronics & Microcontrollers(4) * ECL 3130 IC Fabrication & VLSI(4) * ECP 2070 Digital Electronics Lab(1) * ECP 2201 Electronics Workshop Lab (1) * PCN 1010 NSS(0)

"All efforts have been made to publish this result after checking the entries properly. However, in case of any O&E same shall be rectified as soon as the same is intimated to us (E&OA)".

Prepared by   Checked by   Verified by

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