

## Dr. Kamaldeep

Ph.D.

Specialization: Power Electronics

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### OBJECTIVE

I would like to utilize my lively and energetic attitude in teaching student with great enthusiasm and love to work in an institute that enhances my technical and creative skills and drives me towards a challenging career. With my administrative and institutional abilities, I would also work to perform administrative tasks and responsibilities.

### WORK EXPERIENCE: 2.5 Years

- I have One Year Teaching Experience in SIT, Ismailabad as an Assistant professor, EE Department from July 2009 to July 2010.
- I have Six Month Teaching Experience in BRCM-CET, BAHAL, Hisar as an Assistant professor, EE Department from July 2012 to January 2013.
- I have one year Teaching Experience in Lovely Professional University, Jalandhar as an Assistant professor, EE Department from July 2017 to August 2018.

### TECHNICAL EXPOSURE

- **Ph.D** dissertation “**Investigation and minimization of Losses in New MLI Topology and its application as STATCOM**” in MATLAB as well as Hardware implementation.
- **M. Tech.** dissertation “**Transmission Loss Allocation in Distribution System**” in MATLAB.

### RESEARCH INTEREST

- Multilevel Inverter.
- Renewable Sources.
- FACTS Devices.

## PROFESSIONAL QUALIFICATION

- Ph.D from Punjab Engineering College (PEC), Chandigarh.
- GATE-2013 Qualified with 91.6 percentile.
- M.Tech (Power System) from YMCA University of Science & Technology (Government), Faridabad, Haryana with CGPA 8.2 in 2010-2012.
- B.Tech (EE) from YMCA Engineering College (Government), Faridabad, Haryana with 63% in 2005-2009.
- 12<sup>th</sup> from C.B.S.E. Delhi with 62% in 2005.
- 10<sup>th</sup> from H.S.E.B. Haryana with 81% in 2003.

## AWARDS

- Awarded as best teaching results in the university exams during 2009-2010 SIT, Hisar.
- Awarded as best trainee during six month industrial training at AIRTEL.
- Won Gold Medal in Cricket Tournament organized by Y.M.C.A University in 2009 and 2012.
- Won Silver Medal in Badminton Tournament organized by PEC University of Technology, Chandigarh in 2016.

## PAPER COMMUNICATED IN SCI JOURNAL

1. **Kamaldeep** and J. Kumar, " Harmonic reduction using Particle Swarm Optimization based SHE Modulation Technique in Asymmetrical DC-AC Converter," in **Electric Power Components & Systems** (Under 2<sup>nd</sup> Review) (Impact Factor=1.212)

## PAPER PUBLISHED

1. **Kamaldeep** and J. Kumar, "General Topology for Asymmetrical Multilevel Inverter with Reduced Number of Switches," in **Institution of Engineering and Technology, IET Power Electronics**, Volume 10, pp. 2034-2041, December, 2017. (Impact Factor=3.414)
2. **Kamaldeep** and J. Kumar, "Performance analysis of H-Bridge multilevel inverter using Selective Harmonic Elimination and Nearest Level Control technique," *2015 International Conference on Electrical, Electronics, Signals, Communication and Optimization (EESCO)*, Visakhapatnam, 2015.
3. **Kamaldeep** and J. Kumar, "Switch reduction and performance analysis using different modulation technique in multilevel inverter," *2016 IEEE 1st International Conference on Power Electronics, Intelligent Control and Energy Systems (ICPEICES)*, Delhi, India, 2016.
4. **Kamaldeep** and J. Kumar, "A New Cross Switched Cascaded Multilevel Inverter Topology with Reduced Number of Switches," *2016 IEEE 7<sup>th</sup> Power India International Conference (PIICON)*, Bikaner, India, 2016.

5. **Kamaldeep** and J. Kumar, "A Novel Three Phase 7-Level Asymmetrical Multilevel Inverter with Reduced Number of Power Electronic Switches," *2016 IEEE 7<sup>th</sup> Power India International Conference (PIICON)*, Bikaner, India, 2016.
6. **Kamaldeep** and J. Kumar, "A New 7-Level Asymmetrical Multilevel Inverter with Reduced Number of Sources and Switching Components," *2016 IEEE 7<sup>th</sup> India International Conference on Power Electronics (IICPE-2016)*, Patiala, India, 2016.
7. **Kamaldeep** and Atmaram, "Behavioural Study of Thyristor Control Series Compensator using MATLAB/Simulink" *Electrical System and Energy Technologies (ESET-2013)* (978-93-82880-46-2), pp 309-312 Hamirpur, India.
8. **Kamaldeep**, J. Kumar and Himanshu, "A New General Topology for Asymmetrical Multilevel Inverter with Reduced Number of Switching Components," *2017 IEEE International conference on Recent Development in Control, Automation and Power Engineering (RDCAPE-2017)*, Amity Noida, India, 2017.
9. **Kamaldeep**, J. Kumar and Himanshu, "A Novel approach to Eliminate Low order Harmonics in Asymmetrical Multilevel Inverter," *2017 IEEE International conference on Current Trends in Computer, Electrical, Electronics & Communication (ICCTCEEC-2017)* (978-1-5386-3243-7), Mysore, India, 2017.
10. **Kamaldeep**, J. Kumar and Himanshu, "A New Cascaded Multilevel Inverter Topology with Reduced Number of Switches and Voltage Sources," *2018 IEEE International Conference on Electrical, Electronics, Computers, Communication, Mechanical and Computing (EECCMC-2018)* (978-1-5386-4304-4) Tamilnadu, India, 2018.
11. **Kamaldeep**, J. Kumar and Himanshu, "Asymmetrical Multilevel Inverter Topology with Reduced Number of Components," *2018 IEEE IEEMA Engineer Infinite Conference (eTechNxT)* (978-1-5386-1138-8) India Expo Mart, Greater Noida, Uttar Pradesh, 2018.
12. **Kamaldeep**, J. Kumar and Himanshu, "A New Cascaded Multilevel inverter Topology with Reduced Switch Count," *2018 International Conference on Materials, Applied Physics and Engineering (ICMAE) Indore, Madhya Pradesh, India 2018*.

#### **SKILL SET AND SOFTWARE EXPOSURE**

- Computer Languages : C, C++
- Technical Computing Languages : MATLAB, PROTEUS, dSPACE, TMS28335

#### **EXTRA CURRICULAR ACTIVITIES**

1. Member of organizing committee in International Conference organized by BRCM-CET, Bahal.
2. Member of organizing committee in International Conference organized by PEC University of Technology, Chandigarh.

3. Participated in Cricket tournament organized in YMCA University of Science & Technology Faridabad.
4. Participated in Cricket and Badminton tournament organized in PEC University of Technology, Chandigarh.

**PERSONAL INFORMATION**

Date of Birth : March 02, 1988.  
Father Name : Sh. Ramkumar Boora  
Mother Name : Smt. Rajbala Boora  
Status : Married  
Language : English, Hindi  
Hobbies : Playing Badminton, Cricket, listening Music.

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

(KAMALDEEP)