



श्री माता वैष्णो देवी विश्वविद्यालय

Shri Mata Vaishno Devi University
Kakryal, Katra – 182 320 (J&K)

SMVDU/R&D/2021/5398-5403

Date: 30.09.2021

Corrigendum

Sub: List of candidates provisionally shortlisted for entrance examination and / or interview for admission to Ph.D. Program in Computer Science & Engineering offered by School of Computer Science & Engineering, at SMVDU – Phase II

This is with reference to notification issued vide no. SMVDU/R&D/2021/5375-5380 dated 29.09.2021 regarding the subject cited above. Please read point no. 2 under the heading “Important points” of the notification as under:

The Entrance written test shall consist of Multiple Choice Objective Type Questions [MCQs] of the level of Post Graduate Degree examination in Computer Science & Engineering [50% subject related and 50% Research Methodology]

Rest of the content of the earlier notification shall remain the same.


Dean (R&D)

Copy to

1. Dean FoE for Information
2. Dean [R&D] for Information
3. Head School of Computer Science & Engineering for information and compliance.
4. PS to VC for kind information of the Hon'ble Vice Chancellor.
5. I/c Website, for uploading the same on the University Website.
6. Concerned File