



**Shri Mata Vaishno Devi University**  
**Consolidated Result of M.Tech. (ECE) – 1<sup>st</sup> Year, Second Semester, Dec.-May, 2018**  
**Dated: 06/06/2018**

S. No.	Entry No	Student Name	Sem	cgpa	sgpa	Credits Earned	Backlog List
1	17MMC014	SHIVANI BHAT	2	9.03	9.24	39	Nil
2	17MMC003	DIWANKSHI SHARMA	2	8.90	8.76	39	Nil
3	17MMC018	UMAYIA MUSHTAQ	2	8.77	8.82	39	Nil
4	17MMC016	SYED ZAINAB ALTAF	2	8.44	8.65	39	Nil
5	17MMC002	ATUL BANOTRA	2	8.26	8.24	39	Nil
6	17MMC015	SYED TOOBA SHAMIM ANDRABI	2	8.21	8.29	39	Nil
7	17MMC004	HUMIRAH MAJEED	2	7.87	7.94	39	Nil
8	17MMC005	JAVID AHMAD GANIE	2	7.79	7.29	39	Nil
9	17MMC010	MIR YAVAR HAYAT	2	7.59	7.71	39	Nil
10	17MMC007	MANTISHA GUPTA	2	7.41	7.18	39	Nil
11	17MMC001	AABID RASHID WANI	2	7.38	7.71	39	Nil
12	17MMC012	NAJA MU SAQIB	2	7.23	7.24	39	Nil
13	17MMC006	JYOTI SWAROOP SHARMA	2	7.08	6.59	39	Nil
14	17MMC009	MEHUL RAJESH	2	6.97	6.76	36	* ECL 6051 Microwave Circuit Design( 3)
15	17MMC017	TUFAIL AHMAD LONE	2	6.87	6.65	39	Nil
16	17MMC013	SHAH ZAHID YOUSUF	2	6.72	6.59	39	Nil
17	17MMC008	MASOOD AHMAD MALIK	2	6.41	6.35	39	Nil
18	17MMC011	MOHIT KUMAR	2	6.25	5.94	36	* ECL 6051 Microwave Circuit Design( 3)

Prepared by

Sd/-  
JA (Examination)

Checked by

sd/-  
AR (Examination)

Verified by

sd/-  
Faculty I/c. Examination