



**श्री माता वैष्णो देवी विश्वविद्यालय**  
**SHRI MATA VAISHNO DEVI UNIVERSITY**  
**Consolidated Result of B. Tech (ECE) Batch 2018 3<sup>rd</sup> Semester (withheld & Lateral Entry)**  
**Date: 15-10-2020**

Entry No.	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
18BEC005	ABHISHEK SINGH ANAND	3	5.92	2.41	27	* ECL 1022 Network Analysis & Synthesis( 4) * ECL 1030 Electronic Circuits & Simulation( 4) * ECL 2040 Electromagnetic Field Theory( 4) * ECL 2070 Digital Electronics( 5) * ECL 2151 Analog Communication Engineering( 5) * MEP 1116 Engineering Graphics( 2)* MTL 1025 Engineering Mathematics-I( 3) * MTL 1026 Engineering Mathematics-II( 3) * PCL 2042 Introduction to Logic( 3) * PHL 1012 Engineering Physics( 4)
18BEC008	AMAN SHARMA	3	5.18	1.19	23	* CSL 1028 Programming using Python( 4) * ECL 1001 Introduction to Electronics Engineering( 1) * ECL 1022 Network Analysis & Synthesis( 4) * ECL 1030 Electronic Circuits & Simulation( 4) * ECL 2040 Electromagnetic Field Theory( 4) * ECL 2070 Digital Electronics( 5) * ECL 2151 Analog Communication Engineering( 5) * LNP 1142 Language Lab( 1) * MEP 1043 Engineering Workshop( 1) * MEP 1116 Engineering Graphics ( 2) * MTL 1026 Engineering Mathematics-II( 3) * PCL 2042 Introduction to Logic( 3) * PCN 1010 NSS(0) * PHL 1012 Engineering Physics( 4)
18BEC010	ANUDEEP KOUL	3	4.92	3.55	37	* ECL 1010 Basic Electronics( 4) * ECL 1022 Network Analysis & Synthesis( 4) * ECL 1030 Electronic Circuits & Simulation( 4) * ECL 2040 Electromagnetic Field Theory( 4) * ECL 2151 Analog Communication Engineering( 5) * MEP 1116 Engineering Graphics( 2) *PHL 1012 Engineering Physics( 4)
18BEC014	AVYA KAILA	3	6.76	6.59	64	Nil
18BEC071*	KOMAL	3	5.5	5.5	22	Nil

\*Lateral Entry Case

E&OE

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam