



**श्री माता वैष्णो देवी विश्वविद्यालय**  
**SHRI MATA VAISHNO DEVI UNIVERSITY**  
**Result of B. Tech (ECE) Batch 2016 8<sup>th</sup> Semester E2 (Even semester AY 2019-20)**  
**Date: 23/10/2020**

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
16BEC005	ABHINAV SLATHIA	8	6.19	7.16	165	* ECL 2071 Digital System Design using VHDL( 4) * ECP 2071 VHDL Lab( 1)
16BEC012	ANTRIKSH DEEP	8	6.69	7.87	178	Nil
16BEC067	VAIBHAV MAHAJAN	8	5.82	7.54	170	Nil
16BEC068	VASU SOI	8	5.55	6.48	166	* ECL 2071 Digital System Design using VHDL( 4)
16BEC076	SHIVANSH RAINA	8	6.94	8.19	127	Nil

E&OE

Sd/-

J. A / S. O (Exam)

Sd/-

AR (Exam)

Sd/-

Faculty Coordinator

Sd/-

Faculty I/c Exam