



Shri Mata Vaishno Devi University
Result of M. Tech (ECE) 1st Semester
Batch 2019 (July- December, 2019)

Entry No.	Sem	CGPA	SGPA	Credits Earned	Backlog List
19MMC009	1	8.68	8.68	22	Nil
19MMC011	1	8.5	8.5	22	Nil
19MMC003	1	8.23	8.23	22	Nil
19MMC001	1	7.95	7.95	22	Nil
19MMC008	1	7.86	7.86	22	Nil
19MMC002	1	7.64	7.64	22	Nil
19MMC005	1	7.45	7.45	22	Nil
19MMC010	1	7.09	7.09	22	Nil
19MMC004	1	5.73	5.73	22	Nil
19MMC006	1	5.33	4.36	18	* ECL 6221 Integrated Circuit Design (4)

“Errors & Omissions Expected”

Sd/-

JA / SO (Examination)

Sd/-

AR (Examination)

Sd/-

Faculty Coordinator (Exam)

Sd/-

Faculty I/c. Examination