



**श्री माता वैष्णो देवी विश्वविद्यालय**  
**SHRI MATA VAISHNO DEVI UNIVERSITY**  
**Consolidated Result of B. Tech (CSE) Batch 2017 5<sup>th</sup> Semester (Odd semester AY 2019-20)**  
**(Withheld Cases)**

Entry No	Student Name	SEM	CGPA	SGPA	Credits Earned	Backlog List
17BCS046	SANYA NEGI	5	9.23	8.96	116	Nil
17BCS012	ANIKET THOMAS	5	9.07	9.75	116	Nil
17BCS051	SHIWANI MISHRA	5	8.85	9.08	116	Nil
17BCS013	ANKIT KHANDELWAL	5	8.73	8.38	116	Nil
17BCS043	PURVA SHARMA	5	8.66	8.72	117	Nil
17BCS028	KAVISHA	5	8.64	8.5	116	Nil
17BCS042	PUNIT BATRA	5	8.62	8.25	116	Nil
17BCS047	SARTHAK KAUSHIK	5	7.96	7.68	117	Nil
17BCS057	UDIT DEO	5	7.71	8.64	117	Nil
17BCS019	BHANU PRATAP SINGH	5	7.54	6.71	116	Nil
17BCS036	NIKHIL SHARMA	5	7.44	7.88	116	Nil
17BCS059	VIBHANSH	5	7.07	6.04	113	* MOC 1037 Introduction to Japanese Language and Culture( 3)
17BCS016	ARCHIT DUBEY	5	6.99	7.29	116	Nil
17BCS052	SHOAIB AHMED	5	6.98	7.44	117	Nil
17BCS031	KUMAR ADITYA	5	6.62	7	116	Nil
17BCS056	UDAY DEO	5	5.53	4.32	103	* CSL 3023 Java Programming( 4) * CSL 3032 Design & Algorithm Analysis( 3) * CSL 3101 Artificial Intelligence( 3) * MOC 1033 E-Business( 3)

Prepared by

JA / SO (Examination)

Checked & Verified by

AR (Examination)

Sign

Faculty I/c. Examination



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17BCS006	AGHA SYED NUMAN MURTAZA	5	5.05	2.94	78	* CSL 2031 Data Structures( 3) * CSL 2041 Theory of Computation( 4) * CSL 3023 Java Programming( 4) * CSL 3029 Programming in Python( 4) * CSL 3032 Design & Algorithm Analysis( 4) * CSP 2031 Data Structure Lab( 1) * CSP 3029 Programming in Python Lab( 1)* ECL 2070 Digital Electronics( 4) * ECL 3061 Microprocessor & Interfacing( 3) * ECP 2070 Digital Electronics Lab( 1) * MTL 1012 Calculus & Linear Algebra( 3) * MTL 3021 Probability & Statistics( 3) * PCL 2042 Introduction to Logic( 3)
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*Errors and omissions are expected.*

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