



श्री माता वैष्णो देवी विश्वविद्यालय
SHRI MATA VAISHNO DEVI UNIVERSITY
Consolidated Result of B. Tech (ECE) Batch 2016 7th Semester (Odd semester Ay 2019-20)
(Withheld & Revise cases)

Entry No	Student Name	Sem	CGPA	SGPA	Credits Earned	Backlog List
16BEC076	SHIVANSH RAINA	7	6.42	7.22	117	Nil
16BEC061	SIMRAN CHOUDHARY	7	6.3	7.04	152	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2030 Linear Integrated Circuits & Applications(4)
16BEC077	ADITY PANDOH	7	6.21	6.61	117	Nil
16BEC062	SUKRITI MAHAJAN	7	5.49	4.64	88	* ECL 1022 Network Analysis & Synthesis(4) * ECL 2041 Antenna & Wave Propagation(4) * ECL 2060 Microprocessor Systems(5) * ECL 2071 Digital System Design using VHDL(5) * ECL 3050 Microwave Engineering(5) * ECL 3090 Control Systems(4)
16BEC068	VASU SOI	7	5.26	4.82	137	* ECE 3091 Electronic Measurement & Instrumentation(3) * ECL 2040 Electromagnetic Field Theory(4) * ECL 2071 Digital System Design using VHDL(4) * ECL 3050 Microwave Engineering(5) * ECL 3130 IC Fabrication & VLSI(5) * ECP 1030 Circuits & Simulation Lab(1) * ECP 2070 Digital Electronics Lab(1)
16BEC061	SIMRAN CHOUDHARY	6	6.15	6.27	126	* ECL 1022 Network Analysis & Synthesis(4) * ECL Industrial Electronics (4) * ECL 2030 Linear Integrated Circuits & Applications(4) * PHL 1011 Fundamentals of Physics (3)

Errors and omissions are expected.

Prepared by

JA / SO (Examination)

Checked & Verified by

AR (Examination)

Sign

Faculty I/c. Examination