

श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K)
(A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

Dated: 21 .04.2023

No. SMVDU/R&D/23/1549-1557

NOTIFICATION

It is hereby notified that the Ph.D. thesis of Mr. Neeraj Tripathi (Entry No. 11PHDSECE07), Ph.D. student, School of Electronics & Communication Engineering, Faculty of Engineering, entitled "Modeling and Optimization of Microelectrode for Deep Brain Stimulation" which was submitted under the supervision of Dr. Vipan Kakkar, Associate Professor, School of Electronics & Communication Engineering; for the award of Ph.D. degree, has been accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 13.04.2023, and he is declared eligible for the award of Ph.D. degree w.e.f. 14.04.2023.

The degree will be conferred on him in the next Convocation of the University.

Copy to:

- 1. I/c Dean FoE, for information.
- 2. Head SoECE, for information.
- 3. Finance Officer, for information.
- 4. Dr. Vipan Kakkar, Associate Professor, School of Electronics & Communication Engineering, for information.
- **5.** Assistant Registrar, Vice Chancellor's Secretariat, for kind information of the Hon'ble Vice Chancellor.
- **6.** Faculty I/c Central Library, for information & uploading the final version of thesis on Shodh Ganga website.
- 7. Faculty I/c Website, for uploading on University Website.
- 9. Concerned file/Notification file.