

## श्री माता वैष्णो देवी विश्वविद्यालय SHRI MATA VAISHNO DEVI UNIVERSITY

Kakryal, Katra – 182320 (J&K) (A State University Recognized u/s 2(f) & 12(B) of UGC Act, 1956)

Dated: | 9.01.2023

No. SMVDU/R&D/23/246-253

## **NOTIFICATION**

It is hereby notified that the Ph.D. thesis of Mr. Rajender Kumar (Entry No. 15DEC002), Ph.D. student, School of Electronics & Communication Engineering, Faculty of Engineering, entitled "K-MCSP for Rate Optimal Motion Estimation and Compensation in Constrained Based Environment" which was submitted under the supervision of Dr. Amit Kant Pandit, Associate Professor, School of Electronics & Communication Engineering; for the award of Ph.D. degree, has been accepted on the recommendations of Ph.D. Thesis Defense Committee of the University on 11.01.2023, and he is declared eligible for the award of Ph.D. degree w.e.f. 12.01.2023.

The degree will be conferred on him in the next Convocation of the University.

## Copy to:

- 1. I/c Dean FoE, for information.
- 2. Head SoECE, for information.
- 3. Finance Officer, for information.
- 4. Dr. Amit Kant Pandit, Associate Professor, School of Electronics & Communication Engineering, for information.
- 5. Administrative Officer /PS to VC, for kind information of the Hon'ble Vice Chancellor.
- 6. Faculty I/c Central Library, for information & uploading the final version of thesis on Shodh Ganga website.
- 7. Faculty I/c Website, for uploading on University Website.
- 9. Concerned file/Notification file.